



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re: PATENT application of:

Shunpei YAMAZAKI et al.

Serial No. 09/362,192

Filed: 07/28/1999

For: SEMICONDUCTOR DEVICE HAVING

SEMICONDUCTOR CIRCUIT COMPRISING SEMI-

CONDUCTOR ELEMENT, AND METHOD FOR

MANUFACTURING THE SAME

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Art Unit: 2812

Examiner: V. Simkovic

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FEB 14 2002
TECHNOLOGY CENTER 2800

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231, on 2002.

INFORMATION DISCLOSURE STATEMENT AND
NOTICE OF RELATED APPLICATION

Commissioner for Patents and Trademarks

Washington, D.C. 20231

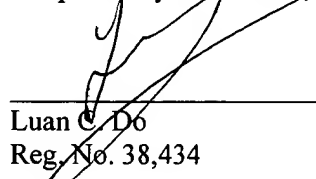
Sir:

In accordance with the provisions of 37 C.F.R. 1.56 and 37 C.F.R. 1.97-1.99, it is requested that the reference(s) listed on the attached Form PTO-1449 be made of record in the above-identified application. Copies of the references are submitted herewith in accordance with 37 C.F.R. 1.98(a).

The Examiner's attention is directed to related application Serial No. 09/259,211.

The Commissioner is hereby authorized to charge fees under 37 C.F.R. §§1.16, 1.17, 1.20(a), 1.20(b), 1.20(c), and 1.20(d) (except the Issue Fee) which may be required now or hereafter, or credit any overpayment to Deposit Account No. 19-2380.

Respectfully submitted,



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